



04-06-05

IFW

UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
**Gordon Ma et al.**

§  
§ Group Art Unit: **2811**

Serial No.: **10/788,815**

§  
§ Examiner: **Nguyen, Cuong Quang**

Filing Date: **February 27, 2004**

§  
§ Attny. Docket No. **068736.0230**

Title: **LDMOS Transistor**

§  
§ Client Ref.: **2003P52733US**

CERTIFICATE OF MAILING VIA EXPRESS MAIL

PURSUANT TO 37 C.F.R. § 1.10, I HEREBY CERTIFY THAT I HAVE INFORMATION AND A  
REASONABLE BASIS FOR BELIEF THAT THIS CORRESPONDENCE WILL BE DEPOSITED  
WITH THE U.S. POSTAL SERVICE AS EXPRESS MAIL POST OFFICE TO ADDRESSEE, ON  
THE DATE BELOW, AND IS ADDRESSED TO:

MAIL STOP RESPONSE  
HONORABLE COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

*Jean Hennessy*  
EXPRESS MAIL LABEL: **EV590023788US**  
DATE OF MAILING: **APRIL 5, 2005**

Sir:

**RESPONSE TO OFFICE ACTION DATED March 21, 2005**

In response to the Examiner's Office Action of March 21, 2005, Applicant submits the following

Remarks.

As the one-month shortened statutory period for reply is due April 21, 2005, this Response is considered timely filed.